

Amendments to the Specification:

Please replace the paragraph beginning at page 1, line 1 through the end of the application, including the abstract with the attached substitute specification in English.

Please replace the paragraph beginning at page 12, line 18, with the following amended paragraph:

FIGS. 76(A) – 76(H) ~~show~~show views of electronic apparatuses to which the invention is applied.

Please replace the paragraph beginning at page 32, line 8, with the following amended paragraph:

At this time, by appropriately setting the current I_{b1} of the basic current source 108, the current I_{b2} of the second basic current source 115, and a transistor size (gate width W , gate length L and the like) of the set transistor 3702 and the charge transistor 3701, a charge accumulated in the capacitor 3704, that is a potential of the gate terminal of the set transistor 3702 (or the charge transistor 3701) is controlled to be approximately equal in the precharge operation and the set operation. Then, in the case where the current I_{b2} of the second basic current source 115 has a larger value than the current I_{b1} of the basic current source 108, the capacitor 3704 can be charged in the precharge operation and a steady state can be obtained rapidly. After that, in the set operation, the steady state can be obtained rapidly even when the current I_{b1} of the basic current source 108 is small in the set operation. This is because the capacitor [[104]] 3704 is almost charged in the precharge operations.

Please replace the paragraph beginning at page 34, line 12, with the following amended paragraph:

Note that the capacitor 3704 is connected to gate terminals of the charge transistor 3701 and the set transistor 3702 and the wiring 3708, however, the invention is not limited to this. It is most preferable that the capacitor 3704 be connected to the gate terminal and a source terminal of the set transistor 3702. This is because the operation of a transistor is not easily influenced by other effects (an effect of a voltage drop and the like due to a wiring resistance and the like) as long as a voltage is maintained between the gate terminal and the source terminal since the operation of the transistor is determined by a gate-source voltage. Provided that the capacitor 3704 is disposed between the gate terminals of the charge transistor 3701 and the set transistor 3702 and another wiring, a potential of the gate terminals of the charge transistor 3701 and the set transistor 3702 may change depending on the level of voltage drop of that another wiring.